

**Amendments to the Specification:**

*Please replace paragraph [0081] with the following rewritten paragraph:*

[0081] Fig. 2, consisting of Figs. 2-1, 2-2, and 2-3, is a drawing illustration of a repeating core;

*Please replace paragraphs [0083] and [0084] with the following rewritten paragraphs :*

[0083] Fig. 3A is a drawing illustration of an area pads;

[0084] Fig. 3B is a pictorial illustration of a area pads [[–]] showing soldering balls;

*Please add the following new paragraph after paragraph [0084]:*

[0084.1] Fig. 3C is an illustration showing area I/Os connected to edge pads using a redistribution layer;

*Please replace paragraph [0105] with the following rewritten paragraph:*

[0105] Fig. 21, consisting of Figs. 21-1, 21-2, and 21-3, is a drawing illustration of very long tracks over a core;

*Please replace paragraph [0117] with the following rewritten paragraph:*

[0117] Fig. 33, consisting of Figs. 33-1 and 33-2, is a drawing illustration of a wafer which comprise continuous array structure by tiling a module;

*Please replace paragraphs [0120] and [0121] with the following rewritten paragraphs:*

[0120] Fig. 36, consisting of Figs. 36-A and 36-B, is a drawing illustration of a repeating module;

[0121] Fig. 37, consisting of Figs. 37-A and 37-B, is a drawing illustration of a repeating module;

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**Amendments to the Drawings:**

The attached sheets of drawings include changes to Figs. 2, 21, 36, and 37, as well as redrawn formal versions of all of the other figures. These sheets should replace all of the originally-filed drawing sheets.

In Fig. 2, the various sub-parts of the drawing have been labeled "Fig. 2-1," "Fig. 2-2," and "Fig. 2-3." Similarly, in Fig. 21, the various sub-parts of the drawing have been labeled "Fig. 21-1," "Fig. 21-2," and "Fig. 21-3." In Figs. 36 and 37, the labels, "Fig. 36" and "Fig. 37," have been deleted from the bottom of the respective sheets.

Attachments: Replacement Sheets

Annotated Sheets Showing Changes